

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Hongyong Zhang Art Unit : 2814
Serial No. : 09/362,808 Examiner : Shrinivas H. Rao
Filed : July 28, 1999 Conf. No. : 7320
Title : METHOD OF FABRICATING SEMICONDUCTOR DEVICE

MAIL STOP RCE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Applicant requests consideration of the references listed on the attached form PTO-1449. Under 37 C.F.R. § 1.98 (a)(2)(ii), only copies of foreign patent documents and/or non-patent literature are enclosed. Copies of any listed U.S. patents or U.S. patent application publications can be provided upon request.

Applicant notes that the Japanese references were cited in the trial decision (also submitted) for Japanese Patent Application No. 2003-70819, which is a Japanese application related to the present application. The U.S. patents are related to JP 07-1310347.

This filing is being made with the filing of a Request for Continued Examination. No fee is required.

Respectfully submitted,

Date: March 4, 2010



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